

The QCCDOC software: Benchmarks, User Environment

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For QCCDOC collaboration

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- Programming Environment
- Operating System (QOS)
- Performance
- Future Activities

Programming Environment

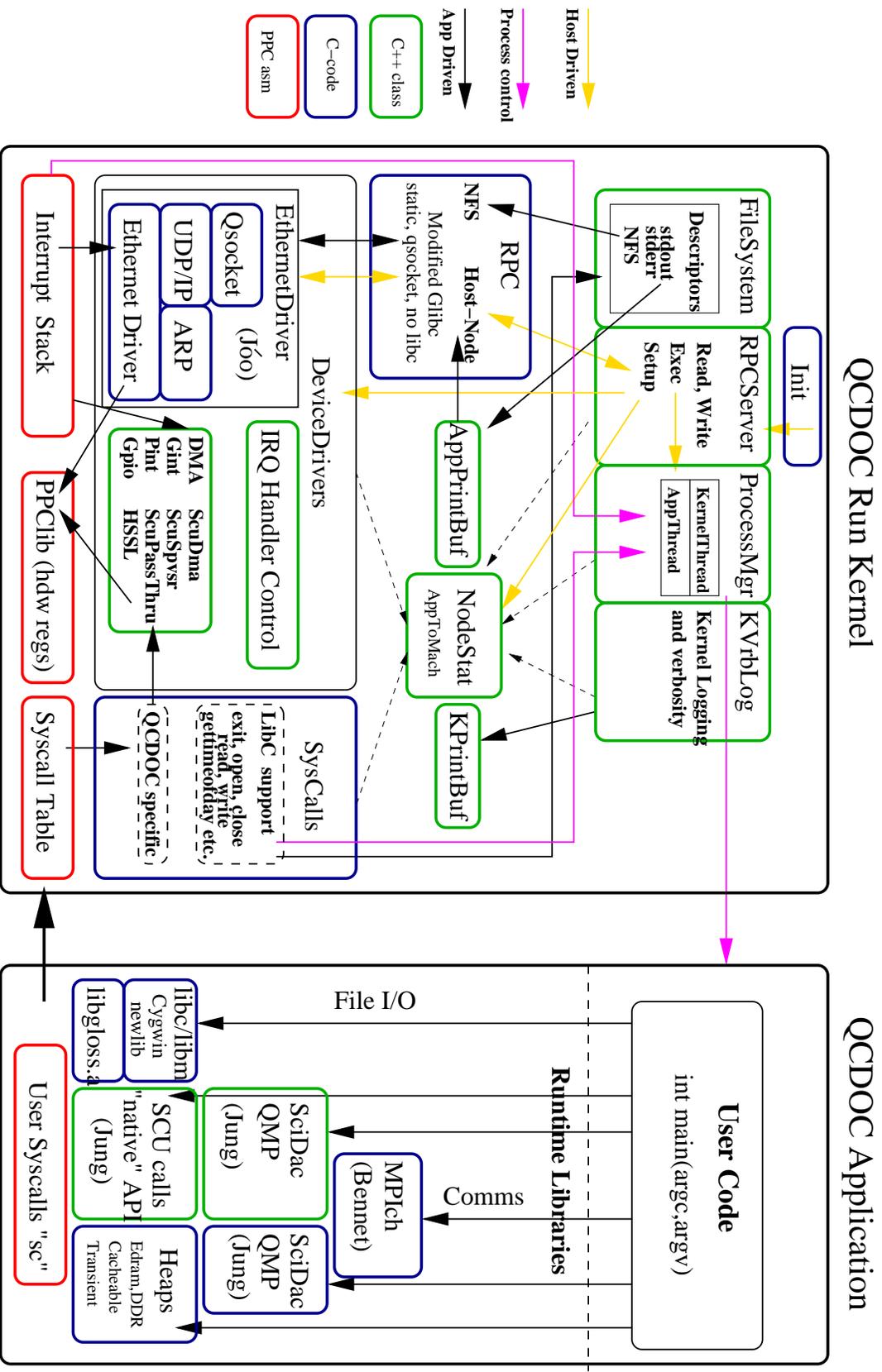
- QOS - Custom-designed Operating system (P. Boyle)
 - Boot (Ethernet/JTAG)
 - Load/Run Apps (Ethernet)
 - Hardware diagnostics
 - Command line argument passing
 - File I/O - NFS heavily tested (currently to the front end)
 - Shell support (qsh : tsh + QCDQC-specific commands) (K. Petrov / P. Boyle)
 - Profiling, Source level debugging
- Standard GNU tools (Compiler, Linker, Binutils...) at the front end
- Commercial PowerPC tools
 - High-performance compilers - XLC
 - Multi-processor debugger - Riscwatch

- SciDAC software
 - QMP - Optimized Message Passing
 - MILC, QDP/C, Chroma, QDP/C++ compiled and tested with QMP
 - Hardware-optimized routines (Level 3 QCD-API)
 - * Integrated in CPS
 - * Being tested with MILC, Chroma

Operating system

- Features:
 - Janus OS - one face to the machine, another to the Unix SMP front-end
 - POSIX Unix SMP front end - linux, solaris, (possibly AIX)
 - Software partitioning
 - Communication remapping from 6 dimensional sublattices to smaller dimensionalities - transparent to users
 - Serve several users and tens of thousands processors
 - Aggressive multi-threading on host side
- Qdaemon - Interface to the QCCDOC back-end
 - RPC over multiple gigE links
 - PBS or Qcsh
- Boot Kernel
 - Controlled boot sequence via Ethernet/JTAG, Ethernet

- **Run kernel:**
 - Run **one process** and run it *well*
 - Memory protection but not translation
 - No scheduling
 - Circular print buffers per each node
 - Service hardware/software interrupts
 - Service communications and I/O requests via System Calls - high degree of separation between user app and run kernel



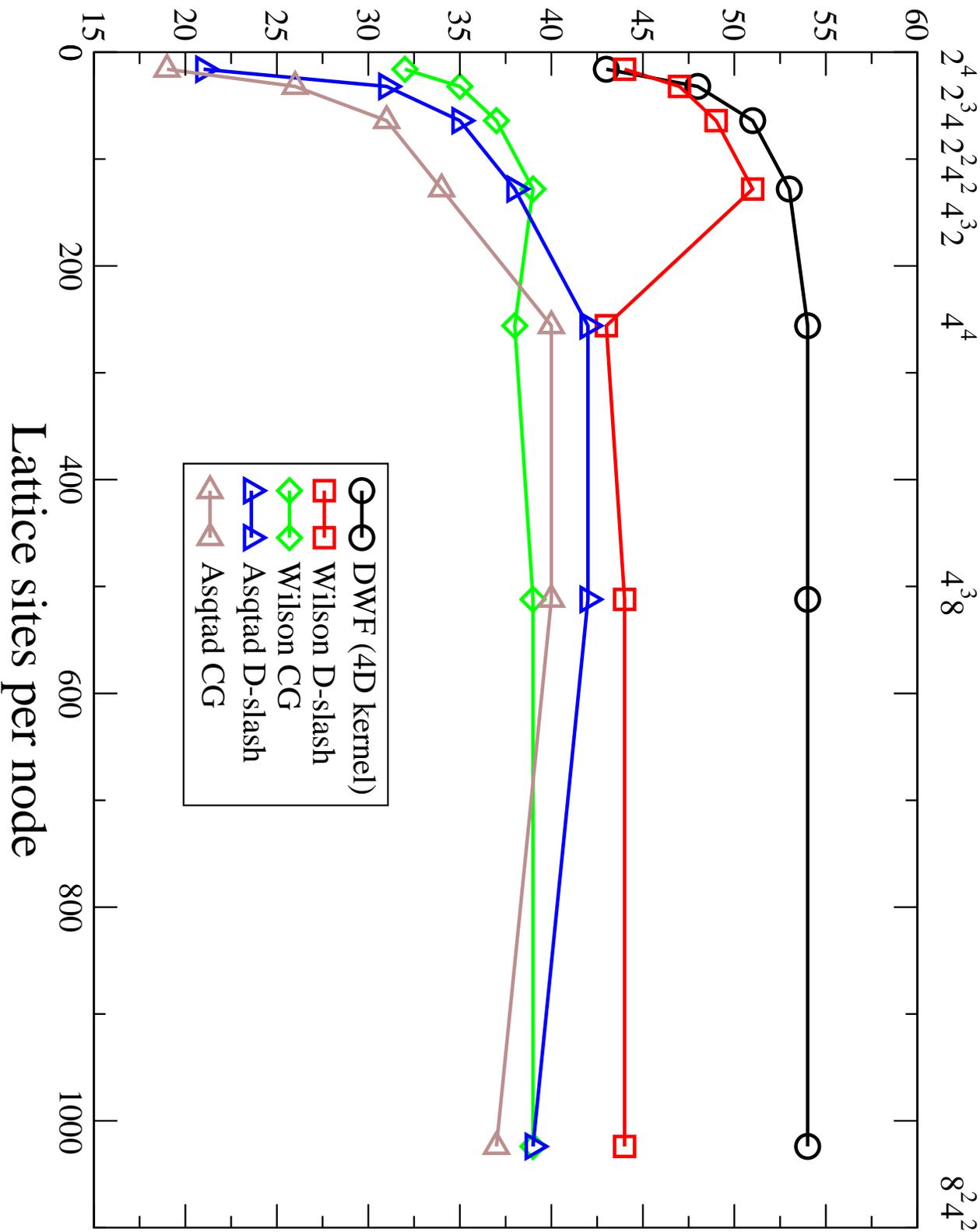
Performance of QCDQC

Tested maximum speed = 450Mhz, Maximum Flops = 900MFlops/s

Application	Sites/node	Optimized		
		D	D	
Machine size		Sim	128	128
Wilson	2 ⁴	47%	44%	32%
	4 ⁴		43%	38%
	6 ⁴		44%	39%
	8 ⁴		29%	
Asqtad	2 ⁴		22%	19%
	4 ⁴	43%	42%	40%
	6 ⁴		35%	33%
	8 ⁴		29%	28%

Performance of QCDOC

Performance as percentage of peak



Performance of QCDQC (extended)

Tested maximum speed = 450Mhz, Maximum Flops = 900MFlops/s

Application	Precision	Sites/node	Optimized			MILC		MILC/QDP
			Double	CG	Sim	Single	Double	Single
Wilson		2 ⁴ 4 ⁴ 6 ⁴ 8 ⁴	Sim	128	128	Sim	16	16
			D	44%	32%			
			D	43%	38%			
Asqtad		2 ⁴ 4 ⁴ 6 ⁴ 8 ⁴	Sim	128	128	Sim	16	16
			D	42%	40%	15%	7%	6%
			D	35%	33%	9%	6%	6%
DWF 4D kernel		2 ⁴ 4 ⁴ 6 ⁴	Sim	128	128	Sim	16	16
			D	43%	32%			
			D	54%	38%			

Planned activities

- QMP & MPI
- Batch system (PBS)
- Parallel file system
- Support for xLC (xcoff)